

## OPEN POSITION – MICROELECTRONICS MIXED-SIGNAL ENGINEER

### ABOUT ARQUIMEA INGENIERÍA

ARQUIMEA INGENIERÍA S.L.U. is a multinational company founded in 2005, with Headquarters in Madrid (Spain), devoted to design and development of electronic, microelectronic and electro-mechanic systems and components, including smart actuators and mechanisms, sensing systems, integrated circuits (IC) and robotic parts. ARQUIMEA's products are suitable for those markets and sectors demanding innovation, quality, high performance and reliability, such as Aerospace, Defence & Security, Automotive or Healthcare.

### JOB DESCRIPTION

We are looking for an Electronics Engineer with experience in Mixed-Signal ASIC design, implementation and test for space applications. The desired skills are:

- Experience in mixed-signal ASIC design at transistor level
- Excellent knowledge of analog and mixed-signal layout
- Experience with Cadence mixed-signal design environment (Composer, ADL, Spectre)
- Experience with Cadence layout tools (Layout L, XL)
- Experience with Matlab for system simulation
- Experience with RTL simulators (Modelsim, etc.) and knowledge of VHDL/Verilog will be a plus
- Experience in mixed-signal circuits validation and testing. Previous laboratory experience is required
- Mixed-signal radiation-hardened design experience is desirable
- Knowledge of the European Space Standards for ASIC/FPGA design is desirable
- Position based in Madrid (Spain)
- Proficient in English. Spanish is a plus
- The candidate must be eligible to work in the EU

**INFORMATION AND APPLICATIONS:** [rrh@arquimea.com](mailto:rrh@arquimea.com)