

OPEN POSITION – ELECTRONICS ENGINEER

JOB DESCRIPTION

We are looking for an Electronics Engineer with experience in FPGA/digital ASIC design, implementation and test for space applications. The desired skills are:

- Based in Frankfurt Oder (Germany)
- Proficiency in VHDL
- Knowledge of the European Space Standards for ASIC/FPGA designs
- Experience with the backend tools of Microsemi (Libero/Designer)
- Proficiency with RTL simulators (Modelsim, etc.)
- Experience in digital circuits testing. Previous laboratory experience is required
- Experience with the Synopsys synthesis and P&R tools for ASICs is a plus
- Proficient in English. German and/or Spanish are a plus
- The candidate must be eligible to work in the EU

ABOUT ARQUIMEA DEUTSCHLAND

ARQUIMEA DEUTSCHLAND GmbH is a German private company founded in 2013, with Headquarters in Frankfurt (Oder) and devoted to design and development of electronics and microelectronics for aerospace applications. ARQUIMEA provides turnkey mixed-signal integrated circuits (ASICs and FPGAs), covering the entire full supply chain from the definition up to delivery of the qualified chip.

ARQUIMEA DEUTSCHLAND GmbH is part of ARQUIMEA GROUP and sister company of ARQUIMEA INGENIERIA, which is a Spanish company with a wide and consolidated background in the design and development of space electronics, microelectronics and mechanisms.

INFORMATION & APPLICATIONS

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