

OPEN POSITION – ELECTRONICS ENGINEER

JOB DESCRIPTION

Following our expansion strategy in Germany, ARQUIMEA is looking for an Electronics Engineer with experience in FPGA/digital ASIC design, implementation and test for space applications. The desired skills are:

- Based in Berlin or Frankfurt Oder (Germany)
- Proficiency in VHDL
- Knowledge of the European Space Standards for ASIC/FPGA designs
- Experience with the backend tools of Microsemi (Libero/Designer)
- Proficiency with RTL simulators (Modelsim, etc.)
- Experience in digital circuits testing. Previous laboratory experience is required
- Experience with the Synopsys synthesis and P&R tools for ASICs is a plus
- Proficient in English. German and/or Spanish are a plus
- The candidate must be eligible to work in the EU

ABOUT ARQUIMEA

ARQUIMEA (www.arquimea.com) is an engineering company founded in 2005, with Headquarters in Madrid (Spain) and a subsidiary in Germany specialized in design, development and commercialization of systems and components qualified for space and hi-rel applications, including actuators and mechanisms, electronic systems and integrated circuits. ARQUIMEA relies on an intensive R&D activity to produce the most innovative products and technologies that are commercialized worldwide.

ARQUIMEA GROUP (www.arquimeagroup.com) is a multinational Holding Corporation with Headquarters in Madrid (Spain) formed by different companies sharing the same passion for technology. Our mission is to deliver useful technology everywhere. At ARQUIMEA GROUP, we think big, do the job and enjoy life.

INFORMATION & APPLICATIONS

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